

Jérôme Hugues
Senior Researcher
Real-Time, Safety-Critical & Embedded Systems
Model-Based System/Software Engineering

June 4, 2020

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A Curriculum Vitae

Jérôme Hugues

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Citizenship: French

A.1 Positions

apr. 2019 -

Senior Researcher – CARNEGIE MELLON / SOFTWARE ENGINEERING INSTITUTE

Member of the Assuring Cyber-Physical Systems Directorate, deputy lead of the Model-Based Engineering initiative.

Research in model-based technologies, with a strong focus on AADL: systems engineering, code generation, application of formal methods for safety-critical systems.

oct. 2009 -

apr. 2019

Professor – ISAE-SUPAERO

Head of third year curriculum in Computer Science, head of the Advanced Master in Embedded Systems. Teaching classes on real-time systems (RTOS, language, scheduling), model-based technologies (SysML, AADL), synchronous languages (SCADE) for the Supaero engineering degree, the Embedded Systems Advanced Master and the Aeronautical and Space Systems Master of Science. Promoted to Professor rank in September 2018.

Research in model-based technologies, with a strong focus on AADL: code generation, application of formal methods for safety-critical systems.

oct. 2009 -

oct. 2005

Associate Professor – TELECOM PARISTECH

Taught classes on real-time systems, synchronous languages (Lustre, Esterel) and software engineering for the Telecom ParisTech engineering curriculum, and the Master of Science SAR, with Université Pierre et Marie Curie. Research activities on distributed systems, real-time systems, use of formal methods in the context of distributed systems.

Keywords:

Model-Based System/Software Engineering, AADL, Distributed, Real-Time, Embedded Systems, Software Engineering, Modeling, formal verification, proof of programs.

A.2 Diploma

2017

“Habilitation à Diriger des Recherches” – INP Toulouse

Summary of my research activities to become full professor, defended in front of a jury of academic Professors.

Topic of the dissertation: *“Architecture in the Service of Real-Time Middleware – Contributions to Architecture Description Languages”*.

2002 - 2005

PhD in Computer Science – Telecom ParisTech

PhD thesis performed in the “Computer and Network” team of the LTCI with Telecom ParisTech. Graduated with honors.

Topic of the dissertation: “*Architecture and services of real-time middleware*” under the supervision of L. Pautet (Telecom ParisTech) and F. Kordon (LIP6/SRC).

Available at <https://www.theses.fr/2005ENST0034>

2001 - 2002 *MSc in Distributed Systems – Université Pierre et Marie Curie*

Classes on distributed systems, concurrency, covering both practical and theoretical aspects.

Master Thesis dissertation on “*MOMA, Message Oriented Middleware for Ada*” under the supervision of L. Pautet (Telecom ParisTech) and F. Kordon (LIP6/SRC).

1999 - 2002 *Telecom ParisTech Engineering Degree*

Classes on computer science, networked systems, signal processing.

Equivalent to a US MSc in Electrical Engineering

B Teaching activities

Prior to joining CMU/SEI, I was Professor with ISAE-Supaero, qualified as “Maître de Conférences” in the Computer Science section or CNU27 in 2006, and as “Professeur” in 2018. My academic career led me to two institutions: first as an associate professor with Telecom ParisTech starting in 2005, then with ISAE-Supaero since 2009 up to 2019.

Note: The following list elements starting from 2009 onwards. The detailed list of classes and topics is available on request.

Aims As a professor, my mission consisted of training engineers for the safety-critical domain. My teaching activities covered multiple aspects of critical systems engineering: languages, methods, tools; both theoretical and practical.

Programs supervision Starting in 2009, I taught courses on real-time systems embedded within ISAE Engineers and Master’s Programs. I was the Head for ISAE of the specialized master “Embedded Systems” and of the last year CS program of the ISAE-Supaero Engineering degree.

Each program trains engineers for the aeronautics domain at large, with a strong focus on safety-critical systems. I participated in the selection of professors from both academia and industry, and the definition of the classes.

Classes taught I taught several classes on embedded systems: real-time systems (RTOS and scheduling), Architecture design using AADL, real-time languages (Ada, RTSJ), distributed systems; along with projects mentoring. Classes are taught either in French or in English to foreign students.

The global volume of face-to-face with students exceeds 200 hours per academic year, for a cumulative cohort of 100+ students.

Teaching Chair From November 2015 to April 2019, I was the recipient of the Thales teaching chair on “Architecture and engineering of embedded Systems” or ARISE. This chair aims at creating an expert track for our engineering students through the creation of advanced training classes, seminars and dedicated projects.

C Research activities

Throughout my career, I have been involved in research activities on model-based techniques in the context of safety-critical systems.

Keywords: AADL, SysML, model-based system/software engineering, safety-critical systems, formal verification, Ada/SPARK, RTOS, ARINC653

C.1 Summary of research activities

My research focuses on “*Architecture of Embedded Systems*” at large: real-time, embedded, cyber-physical or safety-critical. The long term objective is to define 1) unambiguous description mechanisms combined with guided methodology for 2) the analysis and 3) the synthesis of complex systems. These topics are pushed forward through:

Architecture Description Languages – AADL I am a core member of the SAE AS2-C committee working on AADL since 2006. The AADL language is a concise and precise language defined to describe and analyze complex systems. Our contributions aim at transitioning AADL models to executable systems, either through model checking or code generation.

Since 2006, I contribute to the definition of AADLv2 [3] and subsequent revisions, the Data Modeling, Code Generation and ARINC653 annex documents [1, 2]; and the Ocarina AADL toolchain.

Model processing tools – Ocarina My research activities around AADL are validated by the Ocarina toolchain, a model transformation suite developed since 2004. As part of projects and Ph.D. mentoring, we developed transformation towards Petri nets, scheduling analysis tools but also code generation towards high-integrity runtimes.

These contributions have been deployed in various R&D projects with academic and industrial partners: H2020 ESROCOS (with GMV, Airbus Defence and Space, DLR); TASTE (with the European Space Agency, Ellidiss); Flex-eWare (with Thales) or IRT Saint-Exupery.

Middleware for high-integrity systems – PolyORB-HI These highly configurable middleware are built on the “schizophrenic” middleware paradigm, enforcing a strong separation of concerns in middleware constructs, and aligned with requirements for high-integrity systems. PolyORB-HI is combined with code generated from AADL models to target various real-time platforms: Ada and SPARK2014; C RTOS (POSIX, RTEMS, Xenomai, FreeRTOS) or safety-critical OS (VxWorks653, DDC-I or XtratuM).

Summary of contributions Through the careful combination of AADL and code generation, supported by Ocarina and PolyORB-HI, we demonstrated how to reduce the complexity of the infrastructure code to support complex systems. This allows for better analyzability of systems through WCET analysis, proof of programs, while preserving performance and memory footprint.

These contributions have been published as Open Source software, and through academic publications.

C.2 Scientific activities

Standardization committee

2006 – *Member of SAE AS2-C committee, on the Architecture Analysis and Design Language*

Standardization committee work, 4 meetings/year. Redaction of elements of the standard and promotion of AADL through books [25, 26] and tutorials (ESWeek, MODELS, HILT, ...)

Organization of conferences and workshops

- IEEE International Symposium on Rapid System Prototyping (IEEE RSP), General Chair (2009), Program Chair (2008, 2010, 2012), Publicity Chair (2004-), member of the Steering Committee (2007-)
- Architecture Centric Virtual Integration Workshop, Organizer, 2016
- French Summer School on Real-Time systems: organizing team 2009 (Paris) and 2013 (Toulouse), steering committee (2009-)
- Dagstuhl Seminar #12272 “Architecture-Driven Semantic Analysis of Embedded Systems” with Peter Feiler (CMU/SEI) and Oleg Sokolsky (U. Penn.) [9]
- UML&AADL conference 2012, PC Chair
- Reliable Software Technologies 2009, Tutorial Chair

Program committees

- Architecture Centric Virtual Integration Workshop (ACVI), 2014-2016
- International Conference on Software and Systems Engineering and their Applications (ICSSEA), 2011-2013
- International Workshop on Embedded Data-Centric Systems (EDACS), 2009
- Reliable Software Technologies, 2008-2019
- IEEE International Symposium on Object and component-oriented Real-time distributed Computing (IEEE ISORC), 2006-2008
- UML&AADL conference, 2010-2011
- UML&Formal Methods, 2011-2012
- Brazilian Conference on Critical Embedded Systems, 2011-2013
- International Workshop on Analytical Virtual Integration of Cyber-Physical Systems (AVICPS), part of RTSS, 2011-2014
- International Conference on Embedded Software (EMSOFT), 2013
- International Conference on Real-Time Networks and Systems (RTNS), 2013-2017
- Conférence francophone sur l’Architecture Logicielle (CAL), 2014-2016
- International Workshop on Model-Based Architecting and Construction of Embedded Systems (ACES-MB), part of Models, 2013
- EWiLi, Embedded Operating Systems Workshop, part of ESWeek, 2014-2019
- CRTS, Workshop on Compositional Theory and Technology for Real-Time Embedded Systems, part of RTSS, 2017
- FTSCS, International Workshop on Formal Techniques for Safety-Critical Systems, part of IFCEM, 2016, 2018

Journals

- Member of the Editorial Board of Springer “International Journal on Software Tools for Technology Transfer” since 2017
- Editor (2012) with Oleg Sokolsky of a special issue of “Science of Computer Programming” (Elsevier) as a follow-up of the Dagstuhl Seminar #12272 [23]
- Editor (2009) with Kenneth Kent of a special issue of “Software Practice and Experience” (Elsevier), best papers of Rapid System Prototyping 2009, [24]

- Reviewer for ACM TECS, IEEE DS Online, Springer DAEM, Elsevier SCP, Wiley SPE, Springer SoSym, IEEE TDSC

C.3 Invitation to workshops

As part of my scientific contributions, I have been invited to seminars organized by other scientists from the following communities: System Architecture, Software Certification and Cyber-Physical Systems. The following lists are the most relevant ones:

- Software Certification Consortium, every year since 2016, panelist at the 2018 edition (<https://cps-vo.org/node/54714>);
- GeorgiaTech Decision and Control Laboratory seminar, invited by Eric Feron, May 2016, [128]
- Inria, CONVECS team winter seminar, by Hubert Garavel, November 2013, [132],
- Dagstuhl seminar #13051 “Software Certification: Methods and Tools”, [134],
- Dagstuhl seminar #11441 “Science and Engineering of Cyber-Physical Systems”, [135]
- Robotics seminar, UC Berkeley, invited by Edward Lee, [136]
- Journée AdaSpain, invited by Juan Zamorano, [138]

C.4 Grant reviews and panelist

I reviewed project proposals for the following calls:

2017 *Austrian Science Fund (FWF) – doc.funds programme.*

Reviewer

2016 *Agence National de la Recherche/Cofecub*

Reviewer for the ANR CofeCub call for proposals

2011 – 2013 *Agence National de la Recherche*

Reviewer for the ANR SIMI2 call for proposals

I have been panelist in the following calls

2014 - 2017 *Agence Nationale de la Recherche – Generic call for proposals*

“Appel à Projets Générique” (AAPG). Member of phase 1 and 2 panels, project review, sollicitation of external reviews

2011 - 2013 *ANR – Digital Engineering and Security call “Ingénierie Numérique et Sécurité” (INS)*

Member of phase 1 and 2 panels, project review, sollicitation of external reviews

C.5 Research projects

Here is a list of collaborative projects I contributed to.

2008 – *ESA TASTE*

TASTE provides a set of code generation facilities for the space domain. It is lead by the European Space Agency. We provide support for Ocarina and PolyORB-HI to ESA and its partners. ESA funded multiple initiatives to improve Ocarina and PolyORB-HI from 2009 to 2016. This has been pursued by the H2020 ESROCOS project.

- 2018 - 2020 RTRA DAEDALUS
- DAEDALUS explored the interplay between simulation of AADL systems as an extension of code generation, and model checking. In this context, we extended Ocarina to support the AADL Behavioral annex, and extended PolyORB-HI runtime capabilities to be embedded in a simulator and get execution traces to be compared with the symbolic execution through a model checker.
- 2016 - 2018 H2020 ESROCOS
- ESROCOS aims at transitioning TASTE for safe robotics applications. We studied the qualification of the Ocarina toolchain and the PolyORB-HI middleware.
- 2016 - 2017 VIRTUAL SIMULATION OF AVIONICS EMBEDDED PLATFORM: INTEGRATING AADL AND FMI TO EXTEND VIRTUAL INTEGRATION CAPABILITY
- This project evaluated the usage of the Functional Mock-up Interface standard to increase confidence in AADL models through system-level simulations.
- 2016 - 2017 IRT MOISE
- MOISE addressed systems engineering shortcomings in extended company settings. We studied the coupling between architectural models and safety analysis.
- 2014 - 2016 INTEGRATING MODEL-BASED ANALYSIS TO SUPPORT VERIFIABLE SYSTEM COMPOSITION
- This project studied AADL support for code generation for ARINC653 environments, safety analysis. This project has been done in collaboration with CMU/SEI.
- 2015 - 2016 IRT INGEQUIP
- INGEQUIP explored topics in equipment engineering. We focused on software/hardware co-design support using AADL.
- 2013 - 2016 R-TYPE
- R-Type combined refinement and typing techniques inspired from the B method to improve system modeling and proof of correctness in AADL.
- 2011 - 2012 MBSA
- Definition of a modeling methodology using AADL, for Rockwell-Collins use case.
- 2007 - 2009 FUI COUVERTURE
- Tools and methodology for improved code coverage, application to PolyORB-HI.
- 2007 - 2010 FUI-4 MOSIC
- MOSIC – Middleware Ouvert pour Systèmes d'Information Critiques – unified DDS and CCM technologies, and applied verification techniques to these middleware, with Thales Air Systems, PrismTech et OBEO.
- 2007 - 2009 ANR FLEX-EWARE
- Flex-eWare aimed at leveraging component-based approaches for the engineering of embedded systems. We defined the foundations of code generation targeting the C language and RT-Linux.
- 2004 - 2007 FP6-IST ASSERT
- ASSERT defined a process for the construction of space applications, leveraging code generation strategies from AADL models. This project is the root of the Ocarina and PolyORB-HI projects.

C.6 Open Source Software

All my collaborations are gathered in two Open Source software:

2004 – *Ocarina – PolyORB-HI*
<http://www.openaadl.org>

Ocarina is an integrated toolchain for AADL. I am leading the project, with partners from ESA, ENIS and the SEI. Combined with the PolyORB-HI middleware, Ocarina provides support to analyze (schedulability analysis, formal methods) and then generate critical systems. Part of this work led to the definition of AADL annex documents. This project has 200KSLOCs, it has been used in multiple R&D projects at ESA, and by partners from both industry and academia. We are transitioning it towards industry as part of the TASTE and ESROCOS projects, improving code quality and adherence to qualification standards.

2002 – 2007 *PolyORB*
<http://www.adacore.com>

PolyORB is a “schizophrenic” middleware, a generic framework to support multiple standards for distributed systems (CORBA, DSA, MOM, ...). I contributed to this project as part of my Ph.D. work, and my early career.

This software is now a fully supported product by AdaCore, and used by customers like Eurocontrol and Airbus Defence and Space.

C.7 Alumni

PhD students

- Patrick LESERF, 10/2013 - 5/2017, *Optimization of Embedded Systems Architectures in a Model-Based Setting.*, co-advisor with P. de Saqui-Sannes (ISAE).
Publications: [31, 48, 52]
- Guillaume BRAU, 3/2013 - 3/2017, *Architecture Analysis Codesign through Model-Driven Engineering*, co-advisor with N. Navet (Université du Luxembourg).
Publications: [51, 55, 58].
- Gilles LASNIER, 10/2008 - 8/2012, *Integrating Safety and Security in Model-Based Workflows, Towards Verification and Code Generation.*, co-advisor with L. Pautet (Telecom ParisTech/LTCI).
Publications: [69, 71, 79]
- Olivier GILLES, 3/2007 - 4/2010, *Integrating the Target Platform in the Design of Safety-Critical Embedded Systems in a Model-Based Approach.*
Publications: [10, 11, 13, 74, 76, 77, 84, 85]
- Xavier RENAULT, 10/2006 - 12/2009, *Integration of Formal and Semi-formal Models to Implement Distributed Real-time Embedded Systems.*, co-advisor with F. Kordon (LIP6/MoVe).
Publications: [80, 81, 83, 87].
- Bechir ZALILA, 10/2005 - 11/2008, *Configuration and Deployment of Distributed Real-Time Embedded Systems using an ADL.*, co-advisor with L. Pautet (Telecom ParisTech/LTCI).
Publications: [14, 15, 79, 88, 89, 90, 91, 93, 95, 96, 97, 99]

Post-docs

- Rahma BOUAZIZ, 4/2019 - 3/2020, “*Simulation of AADL models*”, as part of the Daedalus project.
- Tatiana PROSVIRNOVA, 1/2016 - 6/2017, “*Coupling of Error Propagation Models and Model-Based Systems Engineering.*”, as part of the IRT MOISE project.
- Bassem OUNI, 3/2015 - 7/2016, “*AADL-based hardware/software codesign workflow.*”, as part of the IRT INGEQUIP project.

- Vincent GAUDEL, 10/2014 - 12/2015, “*Exhaustive simulation of AADL models*”, as part of the TASTE project.

D Publications

D.1 Standards

- [1] SAE AS2-C. SAE Architecture Analysis and Design Language (AADL) Annex Volume 1, Revision A. Standard AS5506/1A, SAE International, 2015.
- [2] SAE AS2-C. SAE Architecture Analysis and Design Language (AADL) Annex Volume 2. Standard AS5506/2, SAE International, 2011.
- [3] SAE AS2-C. Architecture Analysis & Design Language v2.2. Standard AS5506A, SAE International, January 2017.

D.2 Journals

- [4] Hana Mkaouar, Bechir Zalila, Jérôme Hugues, and Mohamed Jmaiel. Towards a formal specification for an aadl behavioural subset using the Int language. *International Journal of Business and Systems Research*, 14(2), 2020. doi:10.1504/IJBSR.2020.106278.
- [5] Hana Mkaouar, Bechir Zalila, Jérôme Hugues, and Mohamed Jmaiel. A formal approach to aadl model-based software engineering. *International Journal on Software Tools for Technology Transfer*, 22(2):219–247, 2020. doi:10.1007/s10009-019-00513-7.
- [6] Patrick Leserf, Pierre de Saqui-Sannes, and Jérôme Hugues. Trade-off analysis for sysml models using decision points and csps. *Software & Systems Modeling*, Jan 2019. doi:10.1007/s10270-019-00717-0.
- [7] Guillaume Brau, Jérôme Hugues, and Nicolas Navet. Towards the systematic analysis of non-functional properties in model-based engineering for real-time embedded systems. *Elsevier Science of Computer Programming*, 156:1–20, 2018. doi:10.1016/j.scico.2017.12.007.
- [8] Wafa Gabsi, Bechir Zalila, and Jérôme Hugues. A development process for the design, implementation and code generation of fault tolerant reconfigurable real time systems. *International Journal of Autonomous and Adaptive Communications Systems (IAACS)*, 9(3/4):269–287, 2016. doi:10.1504/IAACS.2016.079625.
- [9] Peter Feiler, Jérôme Hugues, and Oleg Sokolsky. Architecture-Driven Semantic Analysis of Embedded Systems (Dagstuhl Seminar 12272). *Dagstuhl Reports*, 2(7):30–55, 2012. doi:10.4230/DagRep.2.7.30.
- [10] Olivier Gilles and Jérôme Hugues. A MDE-based optimisation process for Real-Time systems: Optimizing systems at the architecture-level using the real DSL and library of transformation and heuristics. *International Journal of Computer Systems Science & Engineering*, 26(6), November 2011.
- [11] Julien Delange, Olivier Gilles, Jérôme Hugues, and Laurent Pautet. Model-Based Engineering for the Development of Partitioned Architectures. *SAE International Journal of Aerospace*, 3(1):79–86, December 2010. doi:10.4271/2009-01-3234.

- [12] Jorgen Hansson, Bruce Lewis, Jérôme Hugues, Lutz Wrage, Peter Feiler, and John Morley. Model-Based Verification of Security and Non-Functional Behavior using AADL. *IEEE Security & Privacy*, 8(1):43–49, January 2010. doi:10.1109/MSP.2009.143.
- [13] Matteo Bordin, Cyrille Comar, Tristant Gingold, Jérôme Guitton, Olivier Hainque, Thomas Quinot, Julien Delange, Jérôme Hugues, and Laurent Pautet. Couverture: an Innovative Open Framework for Coverage Analysis of Safety Critical Applications. *Ada User Journal*, 30(4):248–255, December 2009.
- [14] Jérôme Hugues, Bechir Zalila, Laurent Pautet, and Fabrice Kordon. From the Prototype to the Final Embedded System Using the Ocarina AADL Tool Suite. *ACM Transactions in Embedded Computing Systems (TECS)*, 7(4):1–25, July 2008. doi:10.1145/1376804.1376810.
- [15] Irfan Hamid, Bechir Zalila, Elie Najm, and Jérôme Hugues. Automatic Framework Generation for Hard Real-time Applications. *Innovations in Systems and Software Engineering: A NASA Journal*, 4(1):107–122, January 2008. doi:10.1007/s11334-008-0044-5.
- [16] Jose A. Pulido, Juan Antonio de la Puente, Jérôme Hugues, Matteo Bordin, and Tullio Vardanega. Ada 2005 code patterns for metamodel-based code generation. *ACM SIGAda Ada Letters*, XXVII(2):53–58, August 2007. doi:10.1145/1316002.1316013.
- [17] Bechir Zalila, Jérôme Hugues, and Laurent Pautet. An Improved IDL Compiler for Optimizing CORBA Applications. *ACM SIGAda Ada Letters*, XXVI(3):21–27, December 2006. doi:10.1145/1185642.1185647.

D.3 Journals, French language

- [18] Jérôme Hugues and Pierre Siron. Ingénierie dirigée par les modèles pour la simulation, le cas de PRISE. *Génie Logiciel*, (109):pp. 38–42, June 2014.
- [19] Janette Cardoso, Jean-Charles Chaudemar, Alexandre Hamez, Jérôme Hugues, and Pierre Siron. Prise : une plate-forme de simulation distribuée pour l’ingénierie des systèmes embarqués. *Génie Logiciel*, (108):pp. 29–34, March 2014.
- [20] Guillaume Rivier, Stéphanie Lizy-Destrez, and Jérôme Hugues. Enseignement de l’ingénierie système : retour sur la conception d’un senseur stellaire pour ESEO et ESMO. *Génie Logiciel – Magazine de l’ingénierie du logiciel et des systèmes*, 100:50–54, June 2012.
- [21] Xavier Renault and Jérôme Hugues. Définition d’une famille de patrons de transformation de modèle pour l’analyse de modèles AADL. *Génie Logiciel – Magazine de l’ingénierie du logiciel et des systèmes*, 93:11–15, June 2010.
- [22] Etienne Borde, Frédéric Gilliers, Grégory Haik, Jérôme Hugues, and Laurent Pautet. MyCCMHI : un framework à composants mettant en oeuvre une approche d’ingénierie dirigée par les modèles. *Génie Logiciel – Magazine de l’ingénierie du logiciel et des systèmes*, 90:6–12, September 2009.

D.4 Special Issues, Editor

- [23] Jérôme Hugues and Oleg Sokolsky. Preface to the special issue: Architecture-driven semantic analysis of embedded systems. *Sci. Comput. Program.*, 106:1–2, 2015. doi:10.1016/j.scico.2015.05.002.
- [24] Kenneth B. Kent and Jérôme Hugues. Editorial to the Special Issue of Rapid System Prototyping’10. *Software: Practice and Experience*, 42(7):779–779, 2012. doi:10.1002/spe.2131.

D.5 Books, editor

- [25] Fabrice Kordon, Jérôme Hugues, Agusti Canals, and Alain Dohet. *Embedded Systems: Analysis and Modeling with SysML, UML and AADL*. Electronic Engineering Series. Wiley-ISTE, April 2013.
- [26] Fabrice Kordon, Jérôme Hugues, Agusti Canals, and Alain Dohet. *Modélisation et analyse de systèmes embarqués*. Collection SEE. Hermes Sciences, Paris, France, April 2013.

D.6 Books

- [27] Emmanuel Grolleau, Jérôme Hugues, Yassine Ouhammou, and Henri Bauer. *Introduction aux systèmes embarqués temps réel : Conception et mise en oeuvre*. Sciences Sup. Dunod, Paris, 2018.
- [28] Francis Cottet, Emmanuel Grolleau, Sébastien Gérard, Jérôme Hugues, Yassine Ouhamou, and Sarah Tucci. *Systèmes temps réel embarqués : spécification, conception, implémentation et validation temporelle*. Technique et Ingénierie, Dunod/L’Usine Nouvelle. Dunod, Paris, 2014. 2ème édition.
- [29] John W. McCornick, Frank Singhoff, and Jérôme Hugues. *Building Parallel, Embedded, and Real-Time Applications with Ada*. Number 9780521197168 in ISBN. Cambridge University Press, April 2011.

D.7 Book chapters

- [30] Jérôme Hugues and Julien Delange. Model-based design and automated validation of arinc653 architectures using the aadl. In Shin Nakajima, Jean-Pierre Talpin, Masumi Toyoshima, and Huafeng Yu, editors, *Cyber-Physical System Design from an Architecture Analysis Viewpoint : Communications of NII Shonan Meetings*, pages pp. 33–52. Springer, 2017.
- [31] Patrick Leserf, Pierre Saqui-Sannes, Jérôme Hugues, and Khaled Chaaban. *Model-Driven Engineering and Software Development: Third International Conference, MODELSWARD 2015, Angers, France, February 9-11, 2015, Revised Selected Papers*, chapter Architecture Optimization with SysML Modeling: A Case Study Using Variability, pages 311–327. Springer International Publishing, Cham, 2015.
- [32] Maxime Perrotin, Julien Delange, and Jérôme Hugues. *Distributed Systems: Design and Algorithms*, chapter Concrete aerospace systems. Number 978-1-84821-250-3 in ISBN. Wiley-ISTE, June 2011.
- [33] Jérôme Hugues, Laurent Pautet, and Fabrice Kordon. *Méthodes Formelles pour les Systèmes Répartis et Coopératifs*, chapter Construction d’un intergiciel vérifié. Number 2-7462-1447-4 in ISBN. October 2006.

D.8 Conferences and workshop

- [34] Léo Creuse, Claire Dross, Christophe Garion, Jérôme Hugues, and Joffrey Huguet. Teaching deductive verification through frama-c and SPARK for non computer scientists. In *Formal Methods Teaching - Third International Workshop and Tutorial, FMTea 2019, Held as Part of the Third World Congress on Formal Methods, FM 2019, Porto, Portugal, October 7, 2019, Proceedings*, pages 23–36, 2019. doi:10.1007/978-3-030-32441-4_2.
- [35] Laura Gouveia, Daniel Silveira, Thanassis Tsiodras, and Jérôme Hugues. Code Coverage On Space Embedded Systems With GCOV. In *ESA DASIA*, 2019.

- [36] Wang Zhe, Jerome Hugues, Jean-Charles Chaudemar, and Thierry LeSergent. An integrated approach to model based engineering with sysml, aadl and face. In *Proceedings of Aerospace Systems and Technology Conference 2018 (ASTC'18)*. SAE International, November 2018. doi:10.4271/2018-01-1942.
- [37] Hana Mkaouar, Bechir Zalila, Jérôme Hugues, and Mohamed Jmaiel. An ocarina extension for AADL formal semantics generation. In *Proceedings of the 33rd Annual ACM Symposium on Applied Computing, SAC 2018, Pau, France, April 09-13, 2018*, pages 1402–1409, 2018. doi:10.1145/3167132.3167282.
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